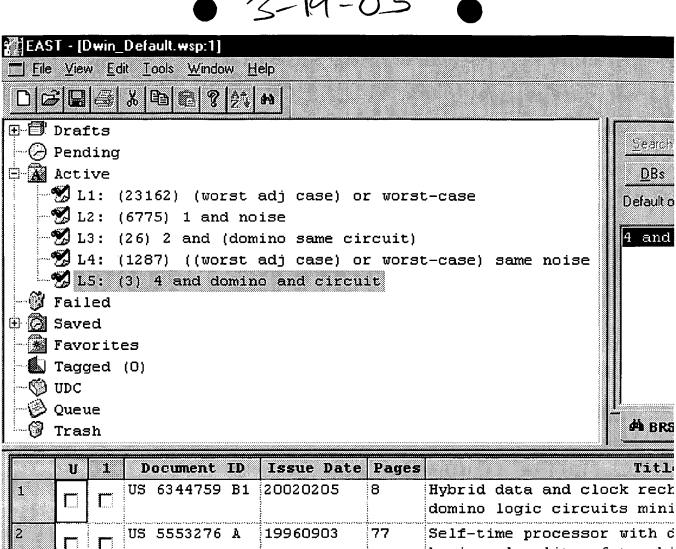
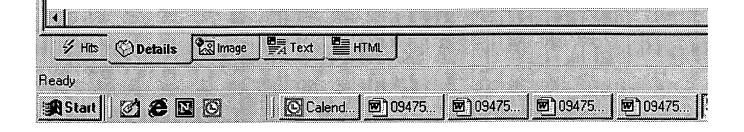
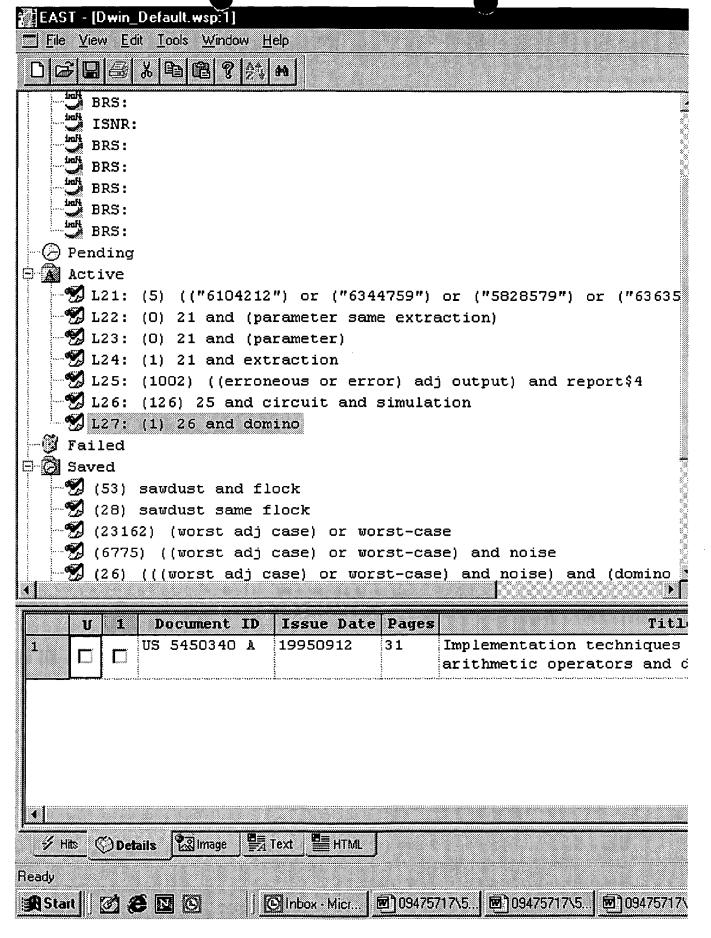
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1	Г		US 6344759 E	31 20020205	•	Hybrid data and clock reck domino logic circuits mini
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4 Transistor stuck-at and delay faults detection in static and dynamic CM combinational gates

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